

AMENDMENTS TO THE CLAIMS

The listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

1. – 12. **(Canceled)**

13. **(Previously Presented)** An optical device, comprising:

an optical transceiver module array system, including:

 a host board;

 a plurality of daughter cards that are each operably connected to the host board; and

 a plurality of transceiver modules;

 a plurality of cages, wherein each cage is connected to a respective one of the plurality of daughter cards, and wherein each cage receives a respective one of the plurality of optoelectronic modules; and

 a latching mechanism that is attached to each transceiver module, comprising:

 a rotatable bail; and

 a pivot block having a lock pin, the pivot block being pivotally attached to the rotatable bail, wherein the lock pin engages a portion of the optical transceiver module array system when the bail and the pivot block are positioned in a specified configuration to selectively secure the transceiver module, wherein each latching mechanism further includes two curved recesses that are defined in surfaces of the respective transceiver module to each movably receive an end portion of the bail.

14. **(Original)** An optical device as defined in claim 13, wherein the lock pin engages a portion of the respective cage that receives the transceiver module.

15. **(Canceled)**

16. **(Previously Presented)** An optical transceiver module array system, comprising:

- a host board;
- a plurality of daughter cards connected to the host board;
- a plurality of transceiver modules;
- a plurality of cages that are each connected to a respective one of the daughter cards, each cage being configured to removably receive a respective one of the transceiver modules; and
- a latching mechanism that is attached to one of the transceiver modules, comprising:
 - a rotatable bail; and
 - two curved recesses that are defined in surfaces of the respective transceiver module to each movably receive an end portion of the bail.

17. **(Previously Presented)** An optical transceiver module array system as defined in claim 16, wherein a connector receptacle is included on each daughter card to electrically connect the transceiver module to the respective host board.

18. **(Previously Presented)** An optical transceiver module array system as defined in claim 16, wherein each cage provides a ground plane for the respective transceiver module.

19. **(Previously Presented)** An optical transceiver module array system as defined in claim 16, wherein each transceiver module includes two optical ports, and the daughter boards are perpendicular to the host board.

20. **(Previously Presented)** An optical transceiver module array system, comprising:
- a host board;
 - a plurality of daughter cards that are each connected to the host board;
 - a plurality of cages that are each connected to a respective one of the daughter cards, wherein each cage provides a ground plane for the respective transceiver module; and
 - a plurality of transceiver modules that are each received by a respective one of the cages;
 - a connector receptacle included on each daughter card to electrically connect the transceiver module to the respective daughter card and host board; and
 - a plurality of optical fiber connectors that are each connected to the optical ports of respective transceiver modules, wherein each optical fiber connector includes a release sleeve that is slidably engaged with the optical fiber connector, wherein the release sleeve includes a body defining open first and second ends, wherein a portion of the body further defines a curved inner surface.

21. **(Original)** An optical transceiver module array system as defined in claim 20, wherein each optical fiber connector is an LC duplex connector having a connector latch for disengaging the LC duplex connector from a respective one of the transceiver modules.

22. **(Original)** An optical transceiver module array system as defined in claim 21, wherein each release sleeve is shaped to correspond to the exterior shape of the respective LC duplex connector.

23. **(Canceled)**

24. **(Previously Presented)** An optical transceiver module array system as defined in claim 20, wherein the release sleeve is selectively slidable between a first position and a second position, and wherein in the second position the curved inner surface engages the connector latch of the LC duplex connector to enable it to disengage from a respective one of the transceiver modules.

25. **(Previously Presented)** An optical transceiver module array system as defined in claim 20, wherein each cage defines a three-sided structure.

26. **(Original)** An optical transceiver module array system as defined in claim 20, wherein at least two of the optical transceivers are positioned on opposing surfaces of one of the daughter cards.

27. **(Previously Presented)** An optical transceiver module array system as defined in claim 20, wherein the release sleeves are composed of a thermoplastic material.

28. **(Currently Amended)** An ~~optoelectronic~~optical transceiver module array system as defined in claim ~~[[1]]~~20, wherein at least one of the plurality of ~~optoelectronic~~transceiver modules is a removable SFP or a XFP transceiver module and includes a single or a double fiber connector.

29. **(Currently Amended)** An ~~optoelectronic~~optical transceiver module array system as defined in claim ~~[[10]]~~16, wherein at least one of the plurality of transceiver modules is a removable SFP or a XFP transceiver module and includes a single or a double fiber connector.

30. **(Currently Amended)** An ~~optoelectronic~~optical transceiver module array system as defined in claim ~~[[4]]~~16, wherein each ~~optoelectronic~~transceiver module includes an outer housing that is received within a respective one of the cages when the ~~optoelectronic~~transceiver modules are removably received within a respective one of the cages.

31. **(Currently Amended)** An ~~optoelectronic~~optical transceiver module array system as defined in claim ~~[[1]]~~16, wherein each ~~optoelectronic~~transceiver module includes an outer housing.

32. **(Currently Amended)** An ~~optoelectronic~~optical device module array ~~system~~ as defined in claim ~~[[1]]~~13, wherein the plurality of daughter cards are each operably connected perpendicularly to the host board, the plurality of daughter cards being positioned parallel to one another; and

wherein the plurality of ~~optoelectronic module~~transceiver modules are each removably connected to the respective one of the daughter cards such that a surface defining the width of each ~~optoelectronic~~transceiver module is positioned parallel to a surface of the respective daughter card, the daughter card surface being perpendicular with respect to the host board.

33. **(Currently Amended)** An optical ~~device~~transceiver module array system as defined in claim ~~[[10]]~~13, wherein each transceiver module includes an outer housing.

34. **(Canceled)**

35. **(Previously Presented)** An optical transceiver module array system as defined in claim 16, wherein the plurality of daughter cards are each perpendicularly connected to the host board, the plurality of daughter cards being positioned parallel to one another; and

wherein the plurality of transceiver modules are each received by a respective one of the cages, wherein the cages and daughter cards are positioned such that spacing between each transceiver module is minimized.

36. **(Previously Presented)** An optical transceiver module array system as defined in claim 20, wherein the plurality of daughter cards are each perpendicularly connected to the host board, the plurality of daughter cards being positioned parallel to one another; and

wherein the cages and daughter cards are positioned such that spacing between each transceiver module is minimized, and wherein each transceiver module includes two optical ports that are oriented with respect to the host board such that an imaginary line that passes through a central portion of both optical ports intersects the host board at substantially a right angle.

37. – 39. **(Canceled)**

40. **(Currently Amended)** An optical ~~device~~transceiver module array system as defined in claim [1]16, wherein the at least two ~~optoelectronic~~transceiver modules are connected to opposite sides of the at least one of the daughter cards.

41. **(Currently Amended)** An optical ~~device~~transceiver module array system as defined in claim [[1]]16, wherein ~~the~~ at least two optoelectronic modules are connected to the same side of ~~the~~ at least one of the daughter cards.

42. **(Currently Amended)** An optical ~~device~~transceiver module array system as defined in claim [[37]]20, wherein ~~the~~ at least two cages are connected to opposite sides of ~~the~~ at least one of the daughter cards.

43. **(Currently Amended)** An optical ~~device~~transceiver module array system as defined in claim ~~[[37]]~~20, wherein ~~the~~-at least two cages are connected to the same side of ~~the~~-at least one of the daughter cards.

44. **(New)** An optical transceiver module array system as defined in claim 16, wherein a daughter card surface of each daughter card includes a connector receptacle for removably receiving a card-edge connector of a respective one of the plurality of transceiver modules.

45. **(New)** An optical transceiver module array system as defined in claim 16, wherein each cage is three-sided and provides electromagnetic shielding for the respective optoelectronic module.

46. **(New)** An optical transceiver module array system as defined in claim 16, wherein the latching mechanism further comprises a lock pin that engages a hole defined in the respective cage to secure the optoelectronic module.